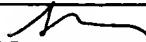


Form PTO-1449			ATTORNEY DOCKET NO. AUS920030484US1	SERIAL NO. Not Assigned	
LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)			APPLICANT Levine et al.		
			FILING DATE	GROUP ART UNIT Not Assigned	
U.S. PATENT DOCUMENTS					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
AYC	AA 5,103,394	Apr. 7, 1992	Blasciak	395/575	Dec. 21, 1989
AYC	AB 6,330,662 B1	Dec. 11, 2001	Patel et al.	712/236	Feb. 23, 1999
AYC	AC 6,480,938 B2	Nov. 12, 2002	Vondran, Jr.	711/125	Dec. 15, 2000
AYC	AD 6,430,741 B1	Aug. 6, 2002	Mattson, Jr. et al.	717/154	Feb. 26, 1999
AYC	AE 6,189,141 B1	Feb. 13, 2001	Benitez et al.	717/4	May 4, 1998
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AYC	AK 5,987,250	Nov. 16, 1999	Subrahmanyam	395/704	Aug. 21, 1997
AYC	AL 6,192,513 B1	Feb. 20, 2001	Subrahmanyam	717/5	Nov. 2, 1998
AYC	AM 5,691,920	Nov. 25, 1997	Levine et al.	364/551.01	Oct. 2, 1995
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AYC	BA 5,966,537	Oct. 12, 1999	Ravichandran	395/709	May 28, 1997
DATE CONSIDERED 8/16/2006			EXAMINER 		

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-1449		ATTORNEY DOCKET NO. AUS920030484US1		SERIAL NO. Not Assigned		
LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)		APPLICANT Levine et al.				
		FILING DATE		GROUP ART UNIT Not Assigned		
U.S. PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE	
AYC	BB	2002/0129309	Sep. 12, 2002	Floyd et al.	714/724	Dec. 18, 2000
AYC	BC	2001/0032305	Oct. 18, 2001	Barry	712/34	Feb. 23, 2001
AYC	BD	2002/0199179	Dec. 26, 2002	Lavery et al.	717/158	Jun. 21, 2001
AYC	BE	2002/0124237	Sep. 5, 2002	Sprunt et al.	717/127	Dec. 29, 2000
AYC	BF	2002/0147965	Oct. 10, 2002	Swaine et al.	717/124	Feb. 1, 2001
AYL	BG	2002/0019976	Feb. 14, 2002	Patel et al.	717/137	May 25, 2001
FOREIGN PATENT DOCUMENTS						
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO	
AYC	BH	JP2000029731	Dec. 8, 1999	Japan	G06F 9/38	X
AYC	BI	JP2000347863	Dec. 15, 2000	Japan	G06F 9/38	X
OTHER PRIOR ART (including author, title, date, pertinent page, etc.)						
AYC	BJ	Kikuchi, "Parallelization Assist System", Joho Shori, Vol. 34, No. 9, Sept. 1993, pp. 1158-1169.				
AYC	BK	Cohen et al., "Hardware-Assisted Characterization of NAS Benchmarks", Cluster Computing, Vol. 4, No. 3, July 2001, pp. 189-196.				
AYC	BL	Talla et al., "Evaluating Signal Processing and Multimedia Applications on SIMD, VLIW and Super Scalar Architectures", International Conference on Computer Design, Austin, Sept. 17-20, 2000, pp. 163-172.				
AYC	BM	Iwasawa et al., "Parallelization Method of Fortran DO Loops by Parallelizing Assist System", Transactions of Information Processing Society of Japan, Vol. 36, No. 8, Aug. 1995-2006.				
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AYC	BO	IBM Research Disclosure Bulletin 444188, "Enable Debuggers as an Objective Performance Measurement Tool for Software Development Cost Reduction", April 2001, pp. 686-688.				
RELATED PATENT APPLICATIONS						
EXAMINER INITIAL	APPLICATION NO/ ATTY. DOCKET NO.	APPLICANT	TITLE		FILING DATE	
AYC	BP 09/435,069 AT9-99-491	Davidson et al.	Method and Apparatus for Instruction Sampling for Performance Monitoring and Debug		Nov. 4, 1999	
AYC	BQ 08/538,071 AUT919950055US1	Gover et al.	Method and System for Selecting and Distinguishing an Event Sequence using an Effective Address in a Processing System		Oct. 2, 1995	
DATE CONSIDERED 8/15/2006		EXAMINER	<i>M</i>			
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.						

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JUN 26 2006

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete If Known	
Application Number	10/674,642
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Chou, Andrew Y.
Art Unit	2192
Attorney Docket No.	AUS920030484US1

FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

NON-PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

Examiner:

Date Considered: 8/17/2008

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Application Number	10/674,642
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Chou, Andrew Y.
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Attorney Docket No.	AUS920030484US1

FOREIGN PATENT DOCUMENTS

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NON-PATENT LITERATURE

Examiner Initial AYL	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. MERTEN et al., "A Hardware Driven Profiling Scheme for Identifying Program Hot Spots to Support Runtime Optimization", IEEE, 1999, pp. 136-147.

Examiner:

Date Considered: 8/17/2016

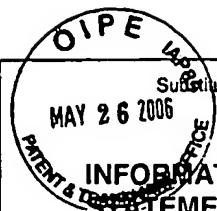
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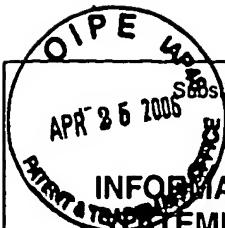
Substitute for form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT			Complete If Known	
			Application Number	10/674,642
			Filing Date	09/30/2003
			First Named Inventor	Levine et al.
			Examiner Name	Das, Chameli
			Art Unit	2192
Attorney Docket No.	AUS920030484US1			
Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AYC	AA 5051944	09-24-1991	Fetterolf et al.	
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FOREIGN PATENT DOCUMENTS				
Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
NON-PATENT LITERATURE				
Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
AYC	AL	JEONG et al., "Cost Sensitive Cache Replacement Algorithms", June 2002, Second Workshop on Caching, Coherence and Consistency, NY, pp. 1-14.		

Examiner: M

Date Considered: 8/17/2008

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Complete If Known	
Application Number	10/674,642
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Das, Chameli
Art Unit	2192
Attorney Docket No.	AUS920030484US1

FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

NON-PATENT LITERATURE

Examiner Initial		Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
AG	AG	Wikipedia "JavaServer Pages" downloaded 1/24/06 http://en.wikipedia.org/wiki/JavaServer_Pages
AH	AH	"Hardware Cycle Based memory Residency", IBM, May 22, 2003, ip.com, ICPOM000012728D, pp. 1-2
AI	AI	HYDE, "The Art of Assembly Language", 2001, Linux Edition, pp. 247-248, retrieved Mar. 1, 2005 from http://webster.cs.ucr.edu/AoA/Linux/PDFs/0_PDFIndexLinux.html

Examiner:

Date Considered: 8/17/2003

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FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

NON-PATENT LITERATURE

Examiner Initial		Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
AYC	AH	TANENBAUM, "Structured Computer Organization", 1984, Prentice-Hall, Inc., 2 nd Edition, pp. 10-12.
AYC	AI	TORRELLAS et al., "False Sharing and Spatial Locality in Multiprocessor Caches", June 1994, IEEE Transactions on Computers, Vol. 43, No. 6, pp. 651-663.
AYC	AJ	ROTHMAN et al., "Analysis of Shared Memory Misses and Reference Patterns", 2000, IEEE, pp. 187-198.

Examiner:

Date Considered:

8/16/2006

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete If Known	
Application Number	10/674,642
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Das, Chameli
Art Unit	2192
Attorney Docket No.	AUS920030484US1

FOREIGN PATENT DOCUMENTS

Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

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Date Considered: 8/16/2006

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INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

Complete If Known

Application Number	10/674,642
Filing Date	09/30/2003
First Named Inventor	Levine et al.
Examiner Name	Das, Chameli
Art Unit	2192
Attorney Docket No.	AUS920030484US1

Examiner Initials	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
AYC AA	4,034,353	07-1977	Denny et al.	
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AYC AF	5,691,920	11-1997	Levine et al.	
AYC AG	5,758,168	05-1998	Mealey et al.	
AYC AH	6,480,966	11-2002	Rawson, III	
AYC AI	2003/0135720	07-2003	DeWitt et al.	
AYC AJ	5,926,640	07-1999	Mason et al.	
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AYC AL	4,145,735	03-1979	Soga	
AYC AM	5,548,762	08-1996	Creedon et al.	
AYC AN	4,794,472	12-1988	Doyama	
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AYC AP	3,707,725	12-1972	Dellheim	
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AYC AV	4,825,359	04-1989	Ohkami et al.	

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Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
AYC AW	Mano, "Computer System Architecture", Prentice-Hall, Inc., 2 nd Ed., 1982, pp.434-443.
AYC AX	Inoue, "Digital mobile communication system designed for nationwide police activities – WIDE system", IEEExplore, October 1996, pp. 1-2.

Examiner: 

Date Considered: 8/16/2006

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Form PTO-1449 LIST OF PRIOR ART CITED BY APPLICANT <small>(Use several sheets if necessary)</small>				ATTORNEY DOCKET NO. AUS920030484US1	SERIAL NO. 10/674,642
				APPLICANT Levine et al.	
				FILING DATE September 30, 2003	GROUP ART UNIT 2192
RELATED PATENT APPLICATIONS					
EXAMINER INITIAL	APPLICATION NO. ATTY. DOCKET NO.	APPLICANT	TITLE		FILING DATE
<i>AVC</i>	AA 10/675,777 / AUS920030477US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction Execution and Data Accesses		Sep. 30, 2003
<i>AVC</i>	AB 10/674,604 / AUS920030478US1	DeWitt, Jr. et al.	Method and Apparatus for Selectively Counting Instructions and Data Accesses		Sep. 30, 2003
<i>AVC</i>	AC 10/675,831 / AUS920030479US1	DeWitt, Jr. et al.	Method and Apparatus for Generating Interrupts Upon Execution of Marked Instructions and Upon Access to Marked Memory Locations		Sep. 30, 2003
<i>AVC</i>	AD 10/675,778 / AUS920030480US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Data Accesses and Instruction Executions that Exceed a Threshold		Sep. 30, 2003
<i>AVC</i>	AE 10/675,776 / AUS920030481US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Execution of Specific Instructions and Accesses to Specific Data Locations		Sep. 30, 2003
<i>AVC</i>	AF 10/675,751 / AUS920030482US1	Levine et al.	Method and Apparatus for Debug Support for Individual Instructions and Memory Locations		Sep. 30, 2003
<i>AVC</i>	AG 10/675,721 / AUS920030483US1	Levine et al.	Method and Apparatus to Autonomically Select Instructions for Selective Counting		Sep. 30, 2003
<i>AVC</i>	AH 10/674,606 / AUS920030485US1	Levine et al.	Method and Apparatus to Autonomically Take an Execution on Specified Instructions		Sep. 30, 2003
<i>AVC</i>	AI 10/675,783 / AUS920030486US1	Levine et al.	Method and Apparatus to Autonomically Profile Applications		Sep. 30, 2003
<i>AVC</i>	AJ 10/675,872 / AUS920030487US1	DeWitt, Jr. et al.	Method and Apparatus for Counting Instruction and Memory Location Ranges		Sep. 30, 2003
DATE CONSIDERED <i>8/16/2002</i>			EXAMINER <i>[Signature]</i>		
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i>	Application Number	10674642
	Filing Date	2003-09-30
	First Named Inventor	Levine et al.
	Art Unit	2192
	Examiner Name	Chou, Andrew Y.
	Attorney Docket Number	AUS920030484US1

U.S.PATENTS						Remove
Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
AYC	1	6944720	B2	2005-09-13	Sperber et al.	
AYC	2	7035996	B2	2006-04-25	Woodall et al.	

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AYC	1	20040030870	A1	2004-02-12	Buser	

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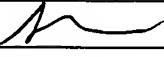
NON-PATENT LITERATURE DOCUMENTS

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i>	Application Number	10674642
	Filing Date	2003-09-30
	First Named Inventor	Levine et al.
	Art Unit	2192
	Examiner Name	Chou, Andrew Y.
	Attorney Docket Number	AUS920030484US1

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T5
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Examiner Signature		Date Considered	8/17/2006
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